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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.		
10/695,785	10/30/2003	Tetsuo Fujii	01-472	7735		
23400 75	590 03/17/2006		EXAMINER			
POSZ LAW C	•	NADA	NADAV, ORI			
12040 SOUTH LAKES DRIVE SUITE 101			ART UNIT	PAPER NUMBER		
RESTON, VA	20191		2811			

DATE MAILED: 03/17/2006

Please find below and/or attached an Office communication concerning this application or proceeding.

Office Action Summary		Application No.		Applicant(s)	
		10/695,785		FUJII, TETSUO	16
		Examiner		Art Unit	
		Ori Nadav		2811	
	The MAILING DATE of this communication app	ears on the cove	r sheet with the c	orrespondence address	
WHIC - Exter after - If NC - Failu Any	ORTENED STATUTORY PERIOD FOR REPLY CHEVER IS LONGER, FROM THE MAILING DANSIONS OF time may be available under the provisions of 37 CFR 1.13 SIX (6) MONTHS from the mailing date of this communication. Or period for reply is specified above, the maximum statutory period we are to reply within the set or extended period for reply will, by statute, reply received by the Office later than three months after the mailing ed patent term adjustment. See 37 CFR 1.704(b).	ATE OF THIS CO 36(a). In no event, how will apply and will expire cause the application	OMMUNICATION ever, may a reply be tim SIX (6) MONTHS.from to become ABANDONE	l. ely filed the mailing date of this communi O (35 U.S.C. § 133).	
Status		•			
2a)	Responsive to communication(s) filed on <u>22 Deserging</u> This action is FINAL. 2b) This Since this application is in condition for allower closed in accordance with the practice under E	action is non-fir	rmal matters, pro		ts is
Dispositi	ion of Claims				
5)□ 6)⊠ 7)□	Claim(s) 1-22 is/are pending in the application. 4a) Of the above claim(s) 3-7 and 10-19 is/are version is/are allowed. Claim(s) 1,2,8,9 and 20-22 is/are rejected. Claim(s) is/are objected to. Claim(s) are subject to restriction and/or	withdrawn from			
Applicati	on Papers				•
10) 11)	The specification is objected to by the Examine The drawing(s) filed on is/are: a) access Applicant may not request that any objection to the or Replacement drawing sheet(s) including the correction of the oath or declaration is objected to by the Examine Theorem 1. The oath or declaration is objected to by the Examine Theorem 2. The oath or declaration is objected to by the Examine Theorem 2. The oath of the oath or declaration is objected to by the Examine Theorem 2. The oath of the oath oath of the oath oath oath oath oath oath oath oath	epted or b) ob drawing(s) be held ion is required if the	in abeyance. See the drawing(s) is obj	37 CFR 1.85(a). ected to. See 37 CFR 1.1	
	ınder 35 U.S.C. § 119				
a)l	Acknowledgment is made of a claim for foreign All b) Some * c) None of: 1. Certified copies of the priority documents 2. Certified copies of the priority documents 3. Copies of the certified copies of the priori application from the International Bureau See the attached detailed Office action for a list of	s have been reco s have been reco ity documents h ı (PCT Rule 17.2	eived. eived in Applicatio ave been receive 2(a)).	on No d in this National Stage	;
A44 - 1		. •			
	e of References Cited (PTO-892)	4)	Interview Summary		
3) Information Pape	e of Draftsperson's Patent Drawing Review (PTO-948) mation Disclosure Statement(s) (PTO-1449 or PTO/SB/08) r No(s)/Mail Date		Paper No(s)/Mail Da Notice of Informal Pa Other:	te atent Application (PTO-152)	
.S. Patent and T	rademark Office				

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DETAILED ACTION

Upon allowance of generic claims, all dependent claims previously withdrawn from consideration under 37 CFR 1.142 will be rejoined.

Claim Rejections - 35 USC § 102

The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless -

(b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.

Claims 1-2, 9 and 20-22 are rejected under 35 U.S.C. 102(b) as being anticipated by Kato (5,773,881).

Regarding claim 20, Kato teaches in figure 7 and related text a dynamic quantity sensor comprising:

a sensor board 23 including a movable portion 22 at one surface side thereof, wherein the movable portion is displaced under application of a dynamic quantity; and

circuit board 31 for communicating with the sensor board, wherein the circuit board is disposed so as to confront one surface of the sensor board through a gap portion 29 and to cover the movable portion, and wherein the sensor board and the circuit board are partially bonded to each other around the gap portion, wherein the movable portion 22 is disposed within a sealed gap 29 that is sealed by sealing the sensor board 23 and circuit board 31.

Regarding claim 1, Kato teaches in figure 7 and related text a dynamic quantity sensor comprising:

a sensor board 23 including a movable portion 22 (located at the left side of 22) at one surface side thereof and a silicon layer (the fixed end of 22 located above pedestal 24 located at the right side of 22) at another surface side thereof, wherein the movable portion is displaced under application of a dynamic quantity and the silicon layer is separated from the movable portion by an insulator (air is an insulator, and the claimed insulator is the air portion located in the groove 2a (see figure 3) between the silicon layer is separated from the movable portion); and

circuit board 31 for communicating with the sensor board, wherein the circuit board is disposed to confront the one surface of the sensor board through a gap portion 29 and to cover the movable portion, and the sensor board and the circuit board are bonded to each other around the gap portion so that a bonding portion is formed that substantially surrounds and seals the gap portion, wherein the gap portion 29 is sealed by sealing the sensor board 23 the circuit board 31 and the gap bonding portion wherein the movable portion is disposed within the sealed gap portion.

Regarding claims 2, 5 and 9, Kato teaches a sensor board and a circuit board are sealingly wrapped by mold material 33, and a lead frame for transmitting electrical signals to an exterior, wherein the sensor board is bonded to the lead frame on another surface opposite to the one surface of the sensor board facing the circuit board.

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Regarding claims 21 and 22, Kato teaches a spacer (the vertical part of circuit board 31 which makes direct contact to sensor board 23) substantially surrounds the gap portion 29, wherein the spacer has a predetermined thickness for separating the circuit board from the movable portion 22.

Claim Rejections - 35 USC § 103

The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negatived by the manner in which the invention was made.

Claim 8 is rejected under 35 U.S.C. 103(a) as being unpatentable over Kato in view of Applicant Admitted Prior Art (AAPA).

Kato teaches in figure 7 and related text substantially the entire claimed structure, as applied to claim 1 above, except the sensor board and the circuit board are electrically connected to each other by bonding wires.

AAPA teaches in figure 19 and related text a sensor board 10 and a circuit board 20 are electrically connected to each other by bonding wire. It would have been obvious to a person of ordinary skill in the art at the time the invention was made to electrically connect the sensor board and the circuit board of Kato's device to each other by bonding wire in order to use the device in an application which requires the inclusion of more passive elements in the circuit board.

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Response to Arguments

Applicant's arguments with respect to claims 1-2, 8-9 and 20-22 have been considered but are most in view of the new ground(s) of rejection.

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Ori Nadav whose telephone number is 571-272-1660. The examiner can normally be reached between the hours of 7 AM to 4 PM (Eastern Standard Time) Monday through Friday.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Eddie Lee can be reached on 571-272-1732. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

O.N. 3/14/06 ORI NADAV
PRIMARY EXAMINER
TECHNOLOGY CENTER 2800